

Notice of References Cited

Application/Control No.

09/699,077

Applicant(s)/Patent Under

Reexamination

ISHIDA ET AL.

Examiner

Ayal I Sharon

Art Unit

2123

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Acuna, E.L. et al. "Simulation Techniques for Mixed Analog / Digital Circuits." IEEE Journal of Solid-State Circuits. Vol.25, Issue 2. pp.353-363.
	V	Carmichael, N. et al. "Simulation as an Aid to Power Supply Diagnostics". AUTOTESTCON '95. Aug. 8-10, 1995. pp.556-560.
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	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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